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Lauren Ballard-Gemmell (Depositor's name) balloud-C (Signature) 11 arch 15,2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/678,751	10/03/2000	John B. Halbert	5038-63	5510

TITLE OF INVENTION: DUAL-PORT BUFFER-TO-MEMORY INTERFACE

	SMALL ENTITY	issue fee		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE			
nonprovisional	NO ·	\$0		\$0	\$0	05/10/2004			
EXAMINER		ART UNIT		CLASS-SUBCLASS	7				
VITAL, PIERRE M		2188		711-172000	J				
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> 1030 SW MORRISON STREET PORTLAND, OREGON 97205 USA

> > (503) 222-3613 FAX: (503) 274-4622

Internet: www.techlaw.com

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James E. Harris Registration No. 40,013

Date:

March 15, 2004

Re:

Attorney Do. No. 5083-063

Number of pages (including this one): 3

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REMARKS:

In re application of: John B. HALBERT et al.

Serial No.: 09/678,751

Examiner:

Pierre M. Vital

Filed:

October 3, 2000

Group Art Unit:

2188

For:

BUFFERED MEMORY MODULE SUPPORTING HIGH-SPEED SYSTEM MEMORY BUS

USING WIDER MEMORY MODULE BUS (as amended)

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PATENT APPLICATION Attorney's Do. No. 5038-63 Intel Ref. P9833

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

John B. Halbert et al.

Serial No.

09/678,751

Examiner:

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Filed:

October 3, 2000

Group Art Unit:

2188

For:

BUFFERED MEMORY MODULE SUPPORTING HIGH-SPEED

SYSTEM MEMORY BUS USING WIDER MEMORY MODULE

BUS (as amended)

Confirm. No. 5518

TRANSMITTAL LETTER

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Enclosed for filing in the above-referenced application are the following:

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Respectfully submitted,

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Lauren Ballard-Gemmell